

Notice of Allowability	Application No.	Applicant(s)	
	10/820,837	SUN, WEIN-TOWN	
	Examiner Jennifer Zubajlo	Art Unit 2629	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to reply filed on 7/5/2007.
2. The allowed claim(s) is/are 1-11.
3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All
 - b) Some*
 - c) None
 of the:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) including changes required by the Notice of Draftperson's Patent Drawing Review (PTO-948) attached
 - 1) hereto or 2) to Paper No./Mail Date _____.
 - (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. Notice of References Cited (PTO-892)
2. Notice of Draftperson's Patent Drawing Review (PTO-948)
3. Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date _____
4. Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. Notice of Informal Patent Application
6. Interview Summary (PTO-413),
Paper No./Mail Date _____.
7. Examiner's Amendment/Comment
8. Examiner's Statement of Reasons for Allowance
9. Other _____.

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EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Jiawei Huang on 8/14/07.

Figures 1A and 1B should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). Corrected drawings in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Examiner has corrected figures 1A and 1B (see attached figures).

Allowable Subject Matter

2. Claims 1-11 are allowed.

The following is an examiner's statement of reasons for allowance: The cited references US 6,933,756, US 6,501,448, and US 2003/0214469 have failed to teach Applicant's claimed invention "**A circuit, for driving a flat panel display, for receiving a data of a current source, a first signal and a second signal and outputting a current via a current output terminal, the circuit comprises: a storage capacitor, having a first terminal and a second terminal, the first terminal being coupled to a system voltage, the second terminal being coupled to a storage voltage; a transmission**

gate, comprising a first N-type transistor and a first P-type transistor, a first source/drain terminal of the first N-type transistor being coupled to a first source/drain terminal of the first P-type transistor serving as a first input/output terminal of the transmission gate, a second source/drain terminal of the first N-type transistor being coupled to a second source/drain terminal of the first P-type transistor serving as a second input/output terminal of the transmission gate, a gate terminal of the first N-type transistor being a first gate terminal of the transmission gate, a gate terminal of the first P-type transistor being a second gate terminal of the transmission gate, the first input/output terminal of the transmission gate being coupled to the storage voltage, the second input/output terminal of the transmission gate being coupled to the data current source, the first gate terminal of the transmission gate being coupled to the first signal, the second gate terminal of the transmission gate being coupled to the second signal; and a current-limiting transistor, wherein a gate terminal of the current-limiting transistor is coupled to the storage voltage, a first source/drain terminal of the current-limiting transistor is coupled to the system voltage, a second source/drain terminal of the current-limiting transistor is coupled to the current output terminal for determining current flow from the current-limiting transistor according to the storage voltage." [Claim 1]. Claims 2-11 are also allowable because claim 1 is generic and allowable.

None of the cited references of record alone or in combination suggests or discloses a circuit for driving a flat panel display comprising: "**a transmission gate,**

comprising a first N-type transistor and a first P-type transistor, a first source/drain terminal of the first N-type transistor being coupled to a first source/drain terminal of the first P-type transistor serving as a first input/output terminal of the transmission gate, a second source/drain terminal of the first N-type transistor being coupled to a second source/drain terminal of the first P-type transistor serving as a second input/output terminal of the transmission gate, a gate terminal of the first N-type transistor being a first gate terminal of the transmission gate, a gate terminal of the first P-type transistor being a second gate terminal of the transmission gate, the first input/output terminal of the transmission gate being coupled to the storage voltage, the second input/output terminal of the transmission gate being coupled to the data current source, the first gate terminal of the transmission gate being coupled to the first signal, the second gate terminal of the transmission gate being coupled to the second signal'.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jennifer Zubajlo whose telephone number is (571) 270-1551. The examiner can normally be reached on Monday-Friday, 8 am - 5 pm, EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amare Mengistu can be reached on (571) 272-7674. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

JZ
8/14/07


AMARE MENGISTU
SUPERVISORY PATENT EXAMINER

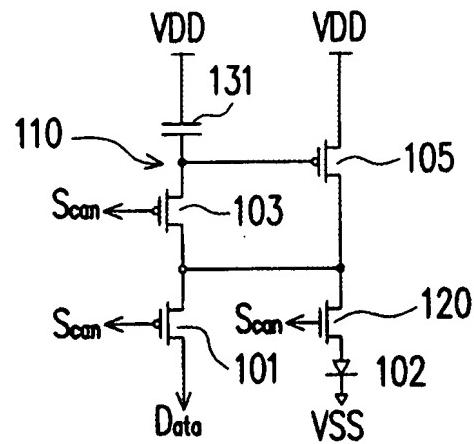


FIG. 1A (PRIOR ART)

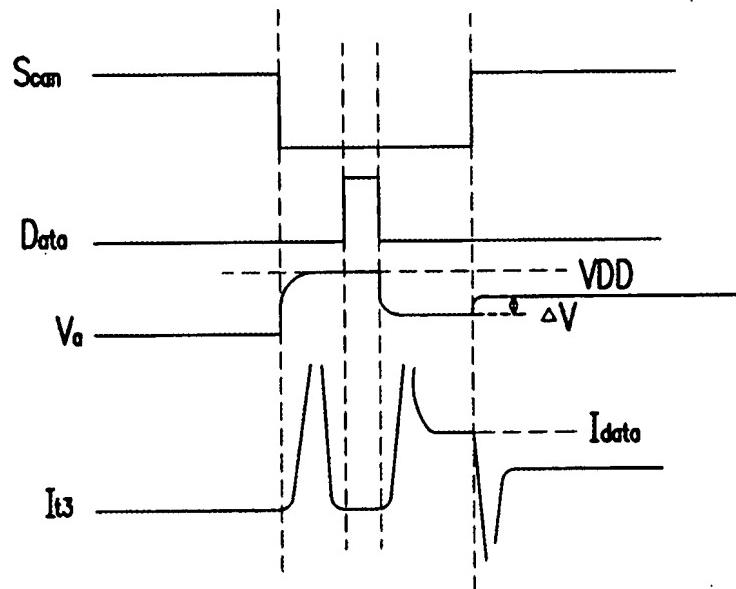


FIG. 1B (PRIOR ART)